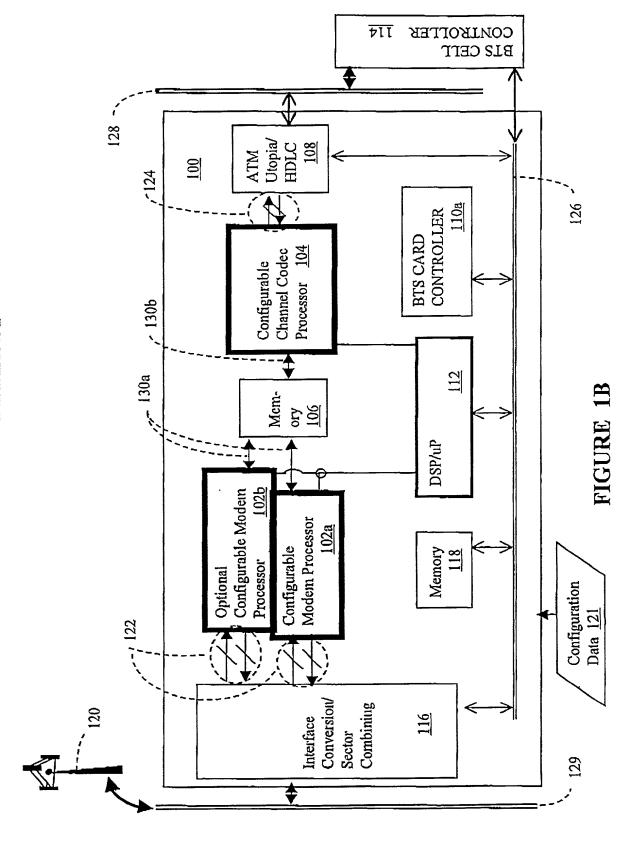


FIGURE 1A



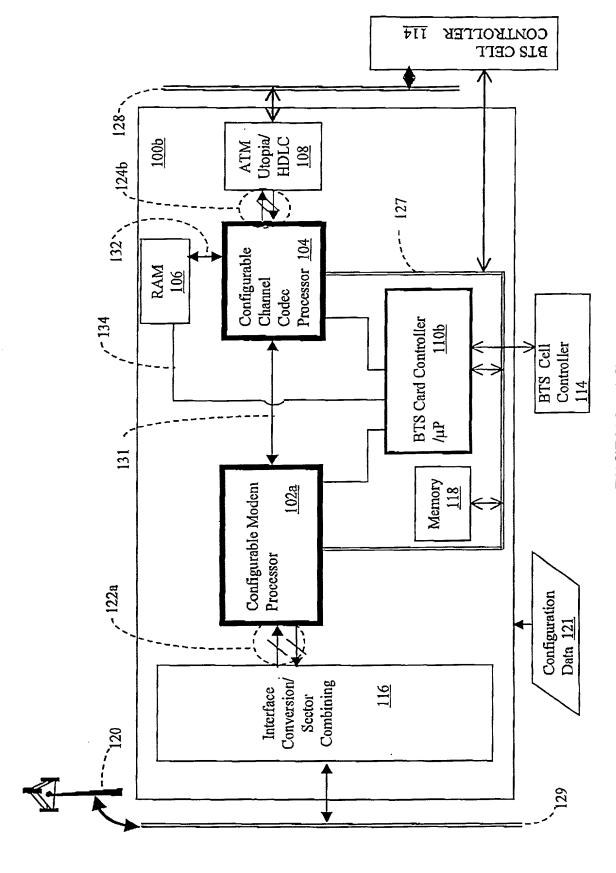
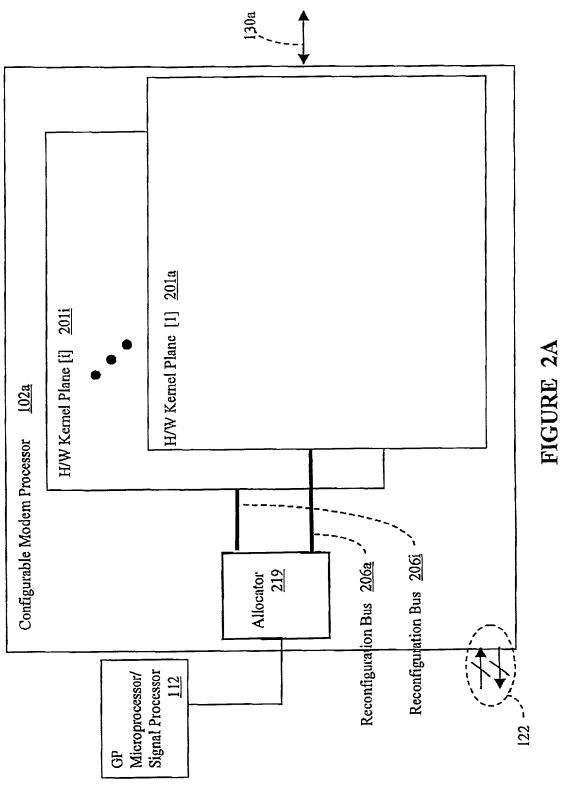


FIGURE 1C

FIGURE 1D



Reconfigurable Logic 241

Memory 240b

Reconfigurable
Dataflow 231

FIGURE 2B

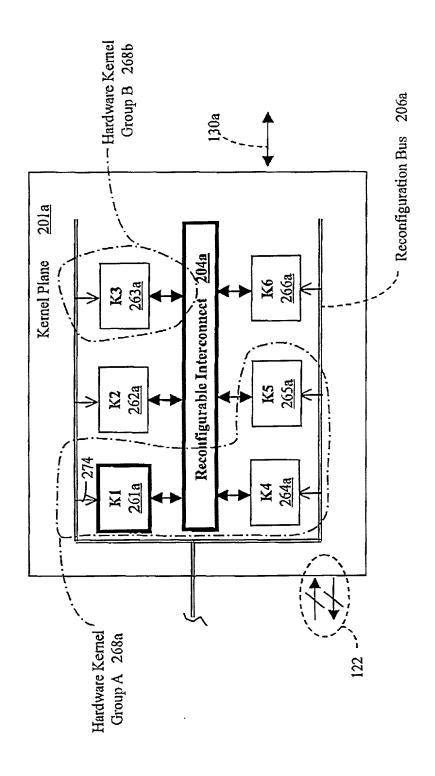


FIGURE 2C

и и тир

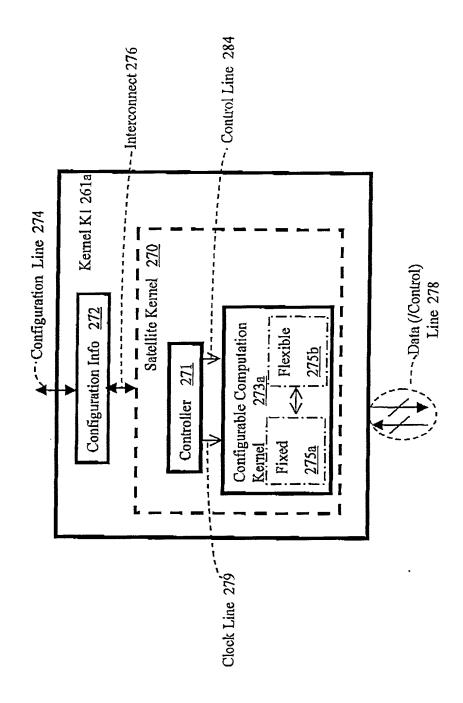


FIGURE 2D

n ing = 0 = 0 = 11

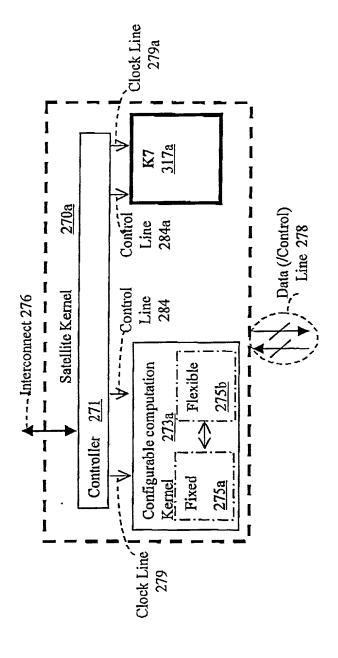


FIGURE 2E

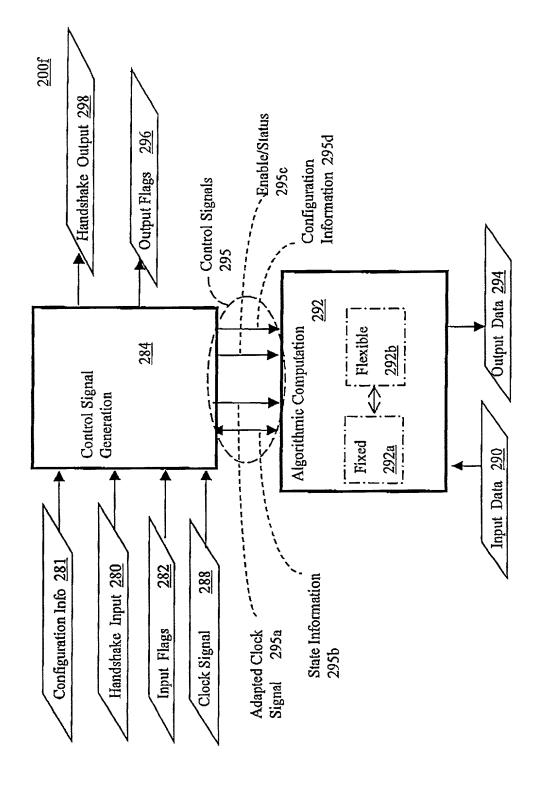


FIGURE 2F

FIGURE 3A

11 1111 III

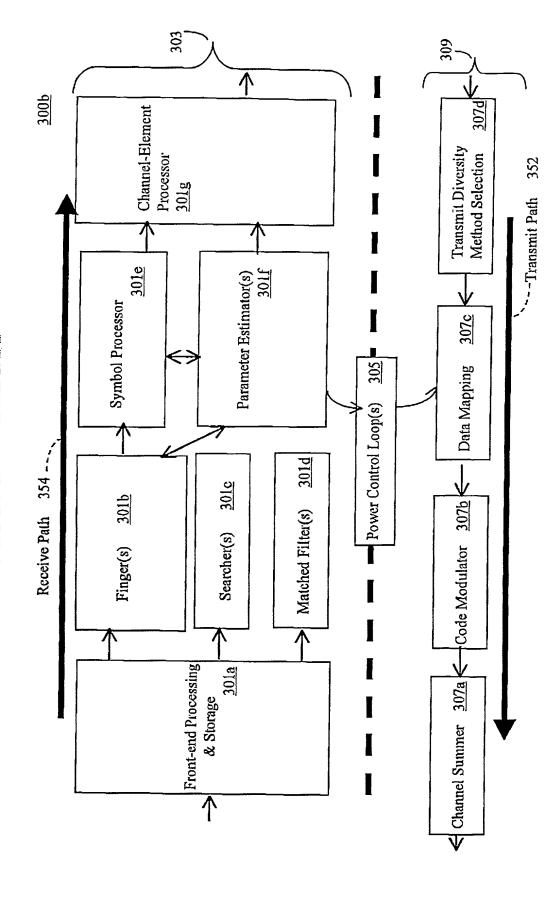
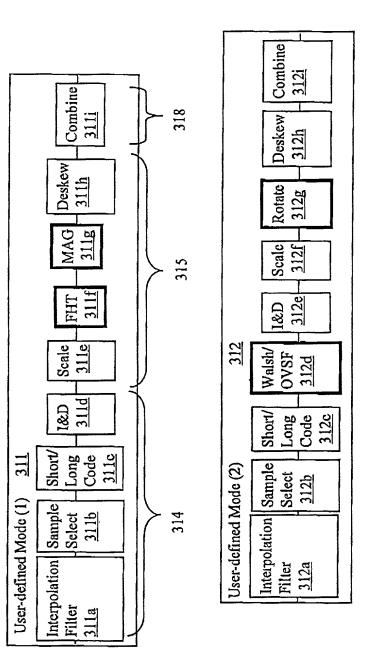


FIGURE 3B



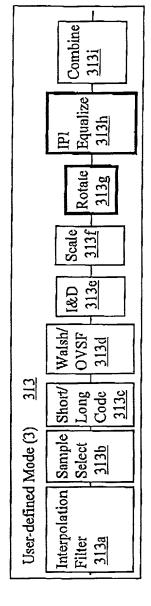


FIGURE 3C

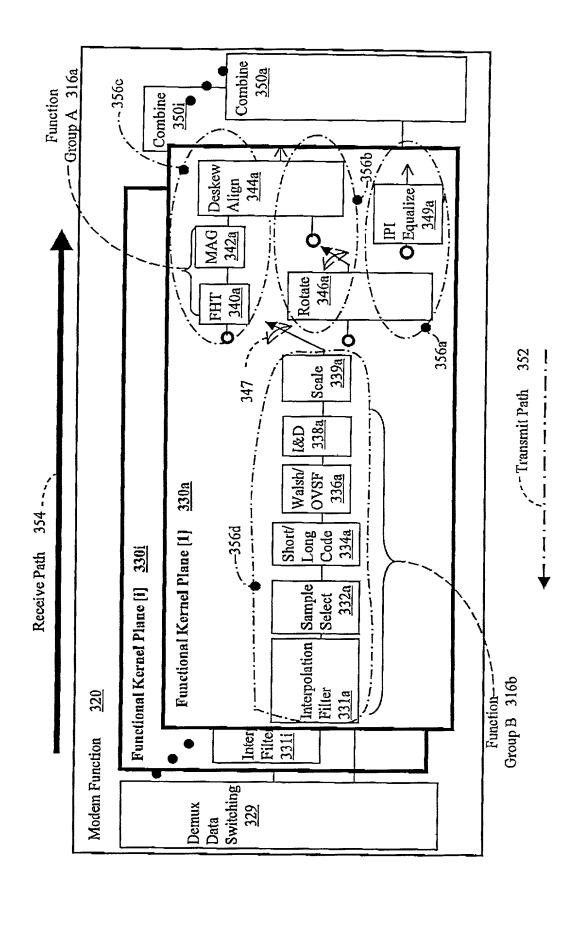


FIGURE 3D

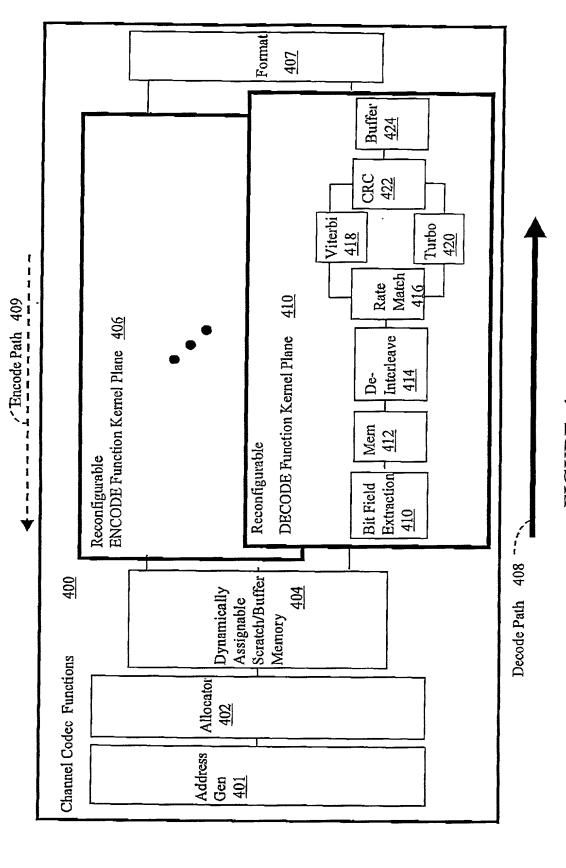


FIGURE 4

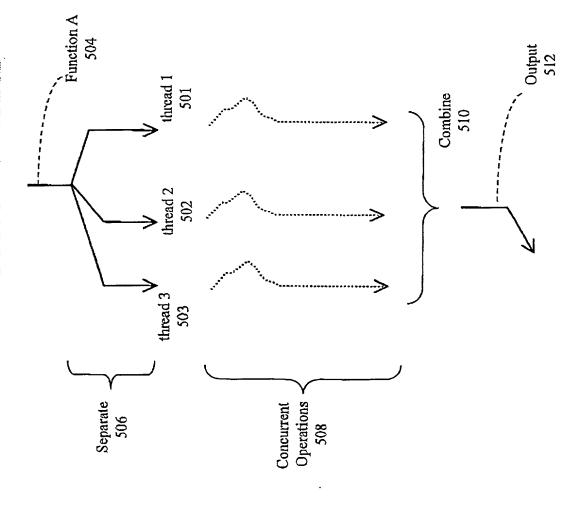


FIGURE 5

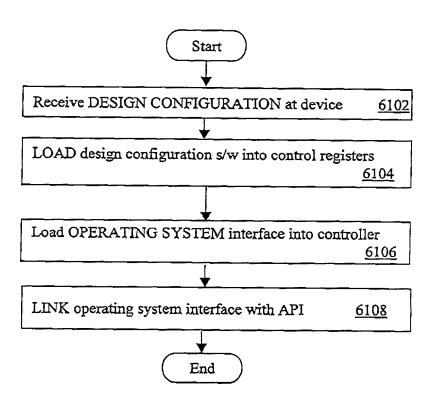


FIGURE 6A

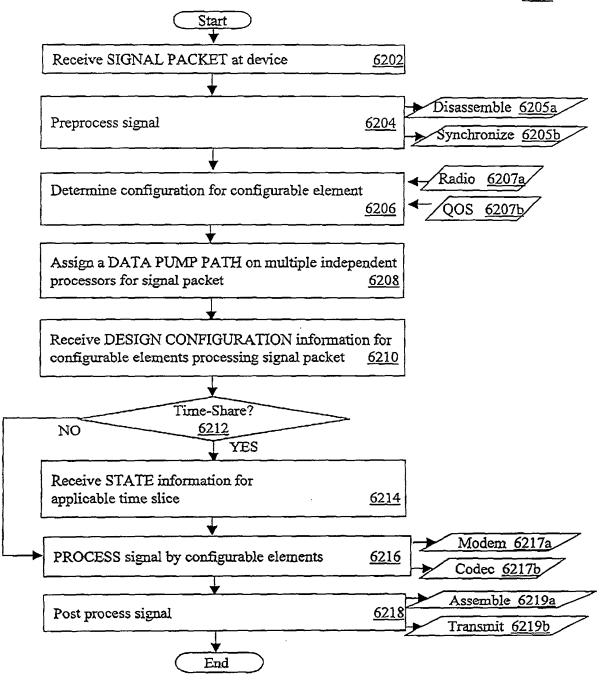


FIGURE 6B